

PRESENTATION OF THE CLAIMS

Although not required under 37 C.F.R. §1.121(c) (because no claims have been amended, added, or canceled), all the claims in their current form are presented below, for the convenience of the examiner and the applicant.

1-26. (Canceled in a previous paper)

27. (Previously Presented) A method of manufacturing a semiconductor component, the method comprising:

providing a leadframe having a surface, the surface defining a horizontal direction and a line perpendicular to the surface defining a vertical direction;

attaching a semiconductor chip to the leadframe;

providing an integrated passive component comprising a surface layer and an interior layer and further comprising a passive device in the interior layer;

attaching the integrated passive component to the semiconductor chip such that:

the semiconductor chip is located between the integrated passive component and the leadframe; and

the semiconductor chip, the integrated passive component, and the leadframe have a vertical relationship with respect to each other; and

disposing a mold compound around the semiconductor chip, the integrated passive component, and the leadframe such that a portion of the leadframe is exposed outside of the mold compound.

28. (Previously Presented) The method of claim 27 wherein:

providing the leadframe comprises:

providing a quad flat non-leaded leadframe.

29. (Original) The method of claim 27 wherein:

attaching the semiconductor chip to the leadframe comprises:

electrically coupling the semiconductor chip to the leadframe using flip chip interconnects.

30. (Original) The method of claim 27 further comprising:

electrically coupling the integrated passive component to at least one of the semiconductor chip and the leadframe.

31. (Original) The method of claim 27 further comprising:

providing an adhesive layer between the integrated passive component and the semiconductor chip; and

securing the integrated passive component to the semiconductor chip using the adhesive layer.

32. (New) The method of claim 27 wherein:

providing the integrated passive component comprises providing a component other than a semiconductor device.

33. (New) The method of claim 30 wherein:

electrically coupling the integrated passive component comprises wire bonding the integrated passive component to at least one of the semiconductor chip and the leadframe.

34. (New) A method of manufacturing a semiconductor component, the method comprising:
- providing a leadframe having a surface, the surface defining a horizontal direction and a line perpendicular to the surface defining a vertical direction;
 - attaching a semiconductor chip having a first surface area to the leadframe;
 - attaching an integrated passive component having a second surface area to the semiconductor chip such that:
 - the semiconductor chip is located between the integrated passive component and the leadframe; and
 - the semiconductor chip, the integrated passive component, and the leadframe have a vertical relationship with respect to each other; and
 - disposing a mold compound around the semiconductor chip, the integrated passive component, and the leadframe such that a portion of the leadframe is exposed outside of the mold compound,
 - wherein:
 - the second surface area is at least as large as the first surface area; and
 - the integrated passive component comprises a passive device.

35. (New) The method of claim 34 wherein:

- providing the leadframe comprises:
 - providing a quad flat non-leaded leadframe.

36. (New) The method of claim 34 wherein:

attaching the semiconductor chip to the leadframe comprises:

electrically coupling the semiconductor chip to the leadframe using flip chip interconnects.

37. (New) The method of claim 34 further comprising:

electrically coupling the integrated passive component to at least one of the semiconductor chip and the leadframe.

38. (New) The method of claim 37 wherein:

electrically coupling the integrated passive component comprises wire bonding the integrated passive component to at least one of the semiconductor chip and the leadframe.

39. (New) The method of claim 34 further comprising:

providing an adhesive layer between the integrated passive component and the semiconductor chip; and

securing the integrated passive component to the semiconductor chip using the adhesive layer.

40. (New) The method of claim 34 wherein:

providing the integrated passive component comprises providing a component other than a semiconductor device.

41. (New) The method of claim 34 wherein:

the second surface area is larger than the first surface area.

42. (New) A method of manufacturing a semiconductor component, the method comprising:

providing a leadframe having a surface, the surface defining a horizontal direction and a line perpendicular to the surface defining a vertical direction;

providing a semiconductor chip having an active side and an inactive side;

attaching the semiconductor chip to the leadframe such that the active side faces the leadframe;

attaching an integrated passive component to the semiconductor chip such that:

the semiconductor chip is located between the integrated passive component and the leadframe;

the inactive side of the semiconductor chip faces the integrated passive component; and

the semiconductor chip, the integrated passive component, and the leadframe have a vertical relationship with respect to each other; and

disposing a mold compound around the semiconductor chip, the integrated passive component, and the leadframe such that a portion of the leadframe is exposed outside of the mold compound,

wherein:

the integrated passive component comprises a passive device.

43. (New) The method of claim 42 wherein:

providing the leadframe comprises:

providing a quad flat non-leaded leadframe.

44. (New) The method of claim 42 wherein:

attaching the semiconductor chip to the leadframe comprises:

electrically coupling the semiconductor chip to the leadframe using flip chip interconnects.

45. (New) The method of claim 42 further comprising:

wire bonding the integrated passive component directly to the leadframe.

46. (New) The method of claim 42 wherein:

providing the integrated passive component comprises providing a component other than a semiconductor device.